IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/653,754 Examiner: Dillon, Samuel A. Filed: September 3, 2003 Group/Art Unit: 2185 Inventor(s): Atty. Dkt. No: 6363-00600 Stephan G. Meier, S. Craig Nelson, and Gene W. Shen Title: Low Power Way-Predicted Cache

RESPONSE TO OFFICE ACTION OF August 6, 2010

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of August 6, 2010, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.